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Overview Programme

Wednesday, August 31st

Registration/ Support Desk hours (08:00 - 13:00 & 14:00 - 17:00)						
Time/Room	Panorama	Megaron G	Phoenix	Atrium A	Atrium B	Atrium C
08:30-09:00	Welcome Session (Organizers, PC Chairs) (Panorama)					
09:00-10:00	Keynote SEAA : Pekka Abrahamsson (Panorama)					
10:00-11:00	SPPI-1	SM-1	SMSE-1	DTFT-1	RECONFIG-1	SDSG-1
11:00-11:30	Coffee Break (DSD Poster Session 1) (Lobby Area)					
11:30-13:00	SPPI-2	SM-2 (11:30-12:45)	MOCS	EPDSD-1	AHSA-1	SOCNOC
13:00-14:30	Lunch Break					
14:30-15:30	Keynote DSD: Dimitrios Serpanos (Panorama)					
15:30-16:30	SPPI-3	SE4SU-1	SMSE-2	MEMO	IMUA	EMBEREA-1
16:30-17:00	Coffee Break (incl. Tool Demos) (Lobby Area)					
17:00-18:00	SPPI-4	SE4SU-2 (17:00-17:45)	SPLECO/TET- DEC	DHCPS-1	DIGI	
19:00-20:00	Welcome Cocktail					

Thursday, September 1st

Registration/ Support Desk hours (08:30 - 13:00 & 14:00 - 17:00)						
Time/Room	Panorama	Megaron G	Phoenix	Atrium A	Atrium B	Atrium C
09:00-10:00	Keynote DSD: Yervant Zorian (Panorama)					
10:00-11:00	CPS-1	ESE-1		SDSG-2	DHCPS-2	EPDSD-2
11:00-11:30	Coffee Break (DSD Poster Session 2) (Lobby Area)					
11:30-13:00	SPPI-5	ESE-2		EPDSD-3	DTFT-2	MCSDIA
13:00-14:30	Lunch Break					
14:30-15:30	Keynote SEAA: Carl-Eric Mols (Panorama)					
15:30-16:30	CPS-2			ETCS	AHSA-2	EMBEREA-2
16:30-17:00	Coffee Break (incl. Tool Demos) (Lobby Area)					
18:00-23:00	Excursion & Conference Dinner					

Friday, September 2nd

Registration/ Support Desk hours (08:30 - 13:00 & 14:00 - 16:00)						
Time/Room	Panorama	Megaron G	Phoenix	Atrium A	Atrium B	Atrium C
09:00-10:00	Keynote DSD: Gustavo Marrero Callico (Panorama)					
10:00-11:00	SMSE-3			DTFT-3	AHSA-3	EPDSD-4
11:00-11:30	Coffee Break (DSD Poster Session 3) (Lobby Area)					
11:30-13:00	A-BPM (11:30-12:45)	ESPRESSE (11:30-12:45)		ASHWPA	RECONFIG-2	ASAIT
13:00-14:30	Lunch Break					
14:30-16:00	SEAA Work in Progress session (14:30-16:10)	DSD Work in Progress session (14:30-16:00)				

	SEAA Sessions
	DSD Sessions
	Keynotes
	Coffee Breaks & Lunches
	Social Events

Registration/ Support Desk hours (08:00 - 13:00 & 14:00 - 17:00)	
08:30-09:00	Welcome Session (Organizers, PC Chairs) (Panorama)
09:00-10:00	Keynote SEAA (Panorama) (Session chair: Carmine Gravino) <i>Dr. Pekka Abrahamsson</i> Software startups are changing the world! What can we learn from them?
10:00-11:00	SPPI-1 (Panorama) - Life-Cycle Management (Session Chair: Dietmar Winkler) – 10:00-10:30 ALM Tool Data Usage in Software Process Metamodeling <i>Petr Picha and Premek Brada</i> – 10:30-11:00 Time to Say 'Good Bye': Feature Lifecycle <i>Aleksander Fabijan, Helena Holmström Olsson and Jan Bosch</i>
10:00-11:00	SM-1 (Megaron G) - Empirical Research in Software Management (Session Chair: Tülin Erçelebi) – 10:00-10:30 Implementing Continuous Customer Care: First-hand Experiences from an Industrial Setting, Aapo Koski <i>Kati Kuusinen, Sampo Suonsyrjä and Tommi Mikkonen</i> – 10:30-11:00 Comparing Test and Production Code Quality in a Large Commercial Multicore System <i>S. Counsell, G. Destefanis, X. Liu, S. Eldh, A. Ermedahl, and K. Andersson</i>
10:00-11:00	SMSE-1 (Phoenix) - Knowledge Management (Session Chair: Steve Counsell) – 10:00-10:30 Systematic Mapping Study of Dealing with Error in Software Development Effort Estimation <i>Idri Ali and El Koutbi Salma</i> – 10:30-11:00 Bayesian Synthesis for Knowledge Translation in Software Engineering: Method and Illustration <i>Deepika Badampudi and Claes Wohlin</i>
10:00-11:00	DTFT-1 (Atrium A) - Dependability, Testing and Fault Tolerance in Digital Systems -1 (Session chair: Andreas Steininger) – Boolean Difference based Reliability Evaluation of Fault-Tolerant Circuit Structures on FPGAs <i>Jahanzeb Anwer and Marco Platzner</i> – The Effect of the Transient Faults in Dependability Prediction <i>Martin Daňhel, Filip Štěpánek and Hana Kubátová</i>
10:00-11:00	RECONFIG-1 (Atrium B) - Reconfigurable Computing-1 (Session chair: Miguel Figueroa) – Automated design flow for bitstream relocation on Xilinx FPGAs <i>André Lalevée, Pierre-Henri Horrein, Matthieu Arzel, Michael Huebner and Sandrine Vatou</i> – A configurable test-processor for board-level testing <i>Jorge Hernan Meza Escobar, Steffen Ostendorff and Heinz-Dietrich Wuttke</i> – Code generation for reconfigurable explicit datapath architectures with LLVM <i>Michaël Adriaansen, Mark Wijtvliet, Roel Jordans, Luc Waeijen and Henk Corporaal</i>
10:00-11:00	SDSG-1 (Atrium C) - System Design for the Smart Grid -1 (Session chair: Emad Samuel Malki Ebeid) – Design of an Event-Driven Residential Demand Response Infrastructure <i>Rune Hylsberg Jacobsen, Armin Ghasem Azar and Emad Samuel Malki Ebeid</i> – A Formal Framework for Modeling Smart Grid Applications: Demand Response Case Study <i>Armin Ghasem Azar, Emad Samuel Malki Ebeid and Rune Hylsberg Jacobsen</i>
11:00-11:30	Coffee Break (DSD Poster Session 1) (Lobby Area)
11:30-13:00	SPPI-2 (Panorama) - Risk Management & Decision Making Support (Session Chair: Konstantin Holl) – 11:30-12:00 Supporting Feature Estimation with Risk and Effort Annotations <i>Simon Grapenthin, Matthias Book, Thomas Richter and Volker Gruhn</i> – 12:00-12:15 Towards Guidelines for Preventing Critical Requirements Engineering Problems <i>Priscilla Mafra, Marcos Kalinowski, Daniel Méndez Fernández, Stefan Wagner and Michael Felderer</i> – 12:15-12:30 Analyzing Performance Issues of Industrial User Interfaces: Experiences and Results <i>Claus Klammer, Rudolf Ramler, Mario Winterer and Heinz Stummer</i> – 12:30-13:00 Providing Tool-Support for Value-Based Decision- Making: A Usability Assessment <i>Vitor Freitas, Emilia Mendes and Burak Turhan</i>

<p>11:30-12:45</p>	<p>SM-2 (Megaron G) - Software Measurement (Session Chair: Leteris Angelis)</p> <ul style="list-style-type: none"> – 11:30-12:00 Estimating and Quantifying the Benefits of Refactoring to Improve a Component Modularity: a Case Study <i>Antonio Martini, Erik Sikander and Niel Madlani</i> – 12:00-12:30 A Validation of a Measurement Based E-government Portals' Maturity Model <i>Abdoullah Fath-Allah, Laila Cheikhi, Rafa Al-Qutaish and Idri Ali</i> – 12:30-12:45 A Case Study on the Utilization of Problem and Solution Domain Measures for Software Size Estimation <i>Tülin Erçelebi Ayyildiz and Altan Kocyigit</i>
<p>11:30-13:00</p>	<p>MOCS (Phoenix) - Model-based Development (Session Chair: Martin Höst)</p> <ul style="list-style-type: none"> – 11:30-12:00 Modeling IoT Applications with SysML4IoT <i>Bruno Costa, Paulo Pires and Flávia Delicato</i> – 12:00-12:30 A Property Model Ontology <i>Sžverine Sentilles, Efi Papatheocharous, Federico Ciccozzi and Kai Petersen</i> – 12:30-13:00 Handling Uncertainty in Automatically Generated Implementation Models in the Automotive Domain <i>Alessio Bucaioni, Antonio Cicchetti, Federico Ciccozzi, Saad Mubeen, Alfonso Pierantonio and Mikael Sjšdin</i>
<p>11:30-13:00</p>	<p>EPDS-1 (Atrium A) - European Projects in Digital System Design - 1 (Session chair: Lech Jozwiak)</p> <ul style="list-style-type: none"> – A software stack for next-generation automotive systems on many-core heterogeneous platforms <i>Paolo Burgio, Marko Bertogna, Ignacio Sanudo Olmedo, Paolo Gai, Andrea Marongiu and Michal Sojka</i> – The ExaNeSt Project: Interconnects, Storage, and Packaging for Exascale Systems <i>Manolis Katevenis, Nikolaos Chrysos, Manolis Marazakis, Iakovos Mavroidis, Fabien Chaix, Nikolaos Kallimanis, Javier Navaridas, John Goodacre, Piero Vicini, Andrea Biagioni, Peter Hopton, Paul Coates, Giuliano Taffoni, Stefano Cozzini, Martin Kersten, Ying Zhang, Julio Sahuquillo, Sergio Lechago, Christian Pinto, Bernd Lietzow, Daniel Everett and Gino Perna</i> – The M2DC Project: Modular Microserver DataCentre <i>Ariel Oleksiak, Michal Kierzynka, Giovanni Agosta, Carlo Brandolese, William Fornaciari, Gerardo Pelosi, Micha Vor Dem Berge, Wolfgang Christmann, Stefan Krupop, Mariano Cecowski, Robert Plestenjak, Justin Cinkelj, Mario Porrman, Jens Hagemeyer, Rene' Griessl, Meysam Peykanu, Lennart Tigges, Loic Cudennec, Thierry Goubier, Jean-Marc Philippe, Sven Rosinger, Daniel Schlitt, Christian Pieper, Chris Adeniyi-Jones and Udo Janssen</i> – A Model-driven Approach for Validating Safe Adaptive Behaviors <i>Mahmoud Hussein, Réda Nouacer and Ansgar Radermacher</i>
<p>11:30-13:00</p>	<p>AHSA-1 (Atrium B) - Architectures and Hardware for Security Applications - 1 (Session chair: Paris Kitsos)</p> <ul style="list-style-type: none"> – Fast and Reliable PUF Response Evaluation from Unsettled Bistable Rings <i>Robert Hesselbarth and Georg Sigl</i> – RTL Implementations and FPGA Benchmarking of Three Authenticated Ciphers Competing in CAESAR Round Two <i>William Diehl and Kris Gaj</i> – X25519 Hardware Implementation for Low-Latency Applications <i>Philipp Koppermann, Fabrizio De Santis, Johann Heyszl and Georg Sigl</i> – Flexible FPGA-Based Architectures for Curve Point Multiplication over GF(p) <i>Dorian Amiet, Andreas Curiger and Paul Zbinden</i>
<p>11:30-13:00</p>	<p>SOCNOC (Atrium C) - Systems and Networks on Chip (Session chair: Alistair McEwan)</p> <ul style="list-style-type: none"> – SlideAcross: A Low-latency Adaptive Router for Chip Multi-processor <i>Wen Zong, Liang Wang, Qiang Xu and Michael Opoku Agyemen</i> – CirKet: A Performance Efficient Hybrid Switching Mechanism for NoC Architectures <i>Mohamad Fallahrad and Ahmad Patooghy</i> – Fault Tolerant Deadlock-free Adaptive Routing Algorithms for Hexagonal Networks-on-Chip <i>Sadia Moriam and Gerhard Fettweis</i> – Adaptive Networks-on-Chip Routing with Fuzzy Logic Control <i>Konstantinos Tatas and Chrysostomos Chrysostomou</i>
<p>13:00-14:30</p>	<p>Lunch Break</p>

14:30-15:30	<p>Keynote DSD (Panorama) (Session chair: Paris Kitsos)</p> <p><i>Prof. Dimitrios Serpanos</i></p> <p>False Data Injection Attacks on Industrial Control Systems</p>
15:30-16:30	<p>SPPI-3 (Panorama) - Software Architecture & Technical Dept (Session Chair: Rudolf Ramler)</p> <ul style="list-style-type: none"> – 15:30-16:00 Busting Software Architecture Beliefs: A Survey on Success Factors in Architecture <i>Decision Making, Jan Salvador van der Ven and Jan Bosch</i> – 16:00-16:15 Technical Debt Management with Genetic Algorithms <i>Sriharsha Vathsavayi and Kari Systä</i> – 16:15-16:30 Modelling Propagation of Technical Debt <i>Johannes Holvitie, Sherlock Licorish and Ville Leppänen</i>
15:30-16:30	<p>SE4SU-1 (Megaron G) - Theory and Concepts in Startup Research (Session Chair: Michel Chaudron)</p> <ul style="list-style-type: none"> – 15:30-16:00 MVP Explained: A Systematic Mapping Study on the Definitions of Minimal Viable Product <i>Valentina Lenarduzzi and Davide Taibi</i> – 16:00-16:30 Towards an early stage software startups evolution model <i>Anh Nguyen Duc, Pekka Abrahamsson and Syed Muhammad Ali Shah</i>
15:30-16:30	<p>SMSE-2 (Phoenix) - Quality (Session Chair: Michael Felderer)</p> <ul style="list-style-type: none"> – 15:30-16:00 Literature Review of Empirical Research Studies within the Domain of Acceptance Testing <i>Johannes Weiss, Ingo Richter Ingo Richter, Peter Mandl</i> – 16:00-16:30 A systematic literature review and a unified model of ATD <i>Terese Besker, Antonio Martini and Jan Bosch</i>
15:30-16:30	<p>MEMO (Atrium A) - Memories (Session chair: Dimitrios Serpanos)</p> <ul style="list-style-type: none"> – Evaluation of Synchronous Dataflow Graph Mappings onto Distributed Memory Architectures <i>Youen Lesparre, Jean-Marc Delosme and Alix Munier</i> – NS-SRAM: Neighborhood Solidarity SRAM For Reliability Enhancement of SRAM Memories <i>Ihsen Alouani, Hamzeh Ahangari, Ozcan Ozturk and Smail Niar</i>
15:30-16:30	<p>IMUA (Atrium B) - Image and Multimedia Applications (Session chair: Amund Skavhaug)</p> <ul style="list-style-type: none"> – A MAC-Enabled High-Performance Low-Power SIMD Architecture <i>Tong Geng, Luc Waeijen, Maurice Peemen, Henk Corporaal and Yifan He</i> – Video SIMDBench: Benchmarking the compiler vectorization for multimedia applications <i>Michail Alvanos and Pedro Trancoso</i>
15:30-16:30	<p>EMBEREA-1 (Atrium C) - Embedded and Real Time systems - 1 (Session chair: Réda Nouacer)</p> <ul style="list-style-type: none"> – Embedded multimodal registration of visible images on long-wave infrared video in real time <i>Fabian Inostroza, Javier Cardenas, Sebastian Godoy and Miguel Figueroa</i> – An embedded hardware architecture for real-time super-resolution in infrared cameras <i>Rodofo Redlich, Luis Araneda, Antonio Saavedra and Miguel Figueroa</i> – HTCC: Haskell to Handel-C Hardware Compiler <i>Ahmed Ablak and Issam Damaj</i>
16:30-17:00	Coffee Break (incl. Tool Demos) (Lobby Area)
17:00-18:00	<p>SPPI-4 (Panorama) - Software Process Improvement (Session Chair: Michael Felderer)</p> <ul style="list-style-type: none"> – 17:00-17:15 Mobile Application Quality Assurance: Reading Scenarios as Inspection and Testing Support <i>Konstantin Holl and Frank Elberzhager</i> – 17:15-17:30 Identifying developers' expertise in social coding platforms <i>Eleni Constantinou and Georgia Kapitsaki</i> – 17:30-17:45 Continuous Integration and Delivery Traceability in Industry: Needs and Practices <i>Daniel Ståhl, Kristofer Hallen and Jan Bosch</i> – 17:45-18:00 Continuous, Lean, and Wasteless: Minimizing Lead Time from Development Done to Production Use <i>Timo Lehtonen, Terhi Kilamo, Sampo Suonsyrjä and Tommi Mikkonen</i>
17:00-17:45	<p>SE4SU-2 (Megaron G) - Lessons Learned from Startup case studies (Session Chair: Michel Chaudron)</p> <ul style="list-style-type: none"> – 17:00-17:30 Product Innovation through Internal Startup in Large Software Companies: a Case Study <i>Henry Edison, Xiaofeng Wang and Pekka Abrahamsson</i> – 17:30-17:45 The Fall and Rise of NimBees <i>Javier Berrocal, Jose García-Alonso and Juan Manuel Murillo</i>

17:00-18:00	<p>SPLECO/TET-DEC (Phoenix) - Software and Education Ecosystems (Session Chair: Erwin Schoitsch)</p> <ul style="list-style-type: none"> – 17:00-17:30 Modelling and Certifying Safety for Cyber-Physical Systems - An educational experiment <i>Xabier Larrucea</i> – 17:30-18:00 Collaborative Innovation: A Model For Selecting The Optimal Ecosystem Innovation Strategy <i>Helena Holmström Olsson and Jan Bosch</i>
17:00-18:00	<p>DHCPS-1 (Atrium A) - Design of Heterogeneous Cyber-Physical Systems -1 (Session chair: Marc Geilen)</p> <ul style="list-style-type: none"> – A Low Latency and Energy Efficient Communication Architecture for Heterogeneous Long-Short Range Communication <i>Fayçal Ait Aoudia, Michele Magno, Matthieu Gautier, Olivier Berder and Luca Benini</i> – The AirSpeck Family of Static and Mobile Wireless Air Quality Monitors <i>D. K. Arvind, Janek Mann, Andrew Bates, and Konstantin Kotsev</i> – A Fast Estimator of Performance with respect to the Design Parameters of Self Re-entrant Flowshops <i>Umar Waqas, Marc Geilen, Sander Stuijk, Joost Van Pinxten, Twan Basten, Lou Somers and Henk Corporaal</i>
17:00-18:00	<p>DIGI (Atrium B) - Digital Design (Session chair: Jeong-A Lee)</p> <ul style="list-style-type: none"> – Dynamic hardware management of the H264/AVC encoder control structure using a framework for system scenarios <i>Yahya Hussain Yassin, Per Gunnar Kjeldsberg, Andrew Perkis and Francky Catthoor</i> – Flexible, Cost-Efficient, High-Throughput Architecture for Layered LDPC Decoders with Fully-Parallel Processing Units <i>Thien Truong Nguyen-Ly, Tushar Gupta, Manuel Pezzin, Valentin Savin, David Declercq and Sorin Cotofana</i> – Design of Efficient 1's Complement Modified Booth Multiplier <i>Kiamal Pekmestzi and Costas Efstathiou</i> – QC-LDPC Gear-Like Decoder Architecture with Multi-Domain Quantization <i>Oana Boncalo</i>
19:00-20:00	Welcome Cocktail

Registration/ Support Desk hours (08:30 - 13:00 & 14:00 - 17:00)	
09:00-10:00	Keynote DSD (Panorama) (Session chair: Lech Jozwiak) <i>Dr. Yervant Zorian</i> Robustness Challenges in Internet of Things
10:00-11:00	CPS-1 (Panorama) - Autonomous Systems (Session Chair: Christian Berger) <ul style="list-style-type: none"> – 10:00-10:30 Leveraging Collective Run-time Adaptation for UAV-based Systems, Darko Bozhinoski <i>Antonio Bucchiarone, Ivano Malavolta, Anna Paola Marconi and Patrizio Pelliccione</i> – 10:30-10:45 Mathematical Test Effort Estimation for Dependability Assessment of Sensor-based Driver Assistance Systems <i>Florian Bock, Sebastian Siegl and Reinhard German</i>
10:00-11:00	ESE-1 (Megaron G) - Embedded Software Requirements and Analysis (Session Chair: Etienne Borde) <ul style="list-style-type: none"> – 10:00-10:30 Combining Requirements, Use Case Maps and AADL Models for Safety-Critical Systems Design <i>Dominique Blouin and Holger Giese</i> – 10:30-11:00 Analysing and Modelling the On-chip Traffic of Parallel Applications <i>Thomas Canhao Xu, Ville Leppänen and Jonne Pohjankukka</i>
10:00-11:00	SDSG-2 (Atrium A) - System Design for the Smart Grid -2 (Session chair: Rune Hylsberg Jacobsen) <ul style="list-style-type: none"> – Multi-modal Building Energy Management System for Residential Demand Response <i>Sergi Rotger-Griful, Ubbe Welling and Rune Hylsberg Jacobsen</i> – Model-Driven Design Approach for Building Smart Grid Applications <i>Emad Samuel Malki Ebeid, Martin Valov and Rune Hylsberg Jacobsen</i> – CoFELS: Conceptual Framework for Electricity Load Shifting System Design <i>Jung Min Kim, Rune Hylsberg Jacobsen and Robert S. Brewer</i>
10:00-11:00	DHPCS-2 (Atrium B) - Design of Heterogeneous Cyber-Physical Systems -2 (Session chair: Marc Geilen) <ul style="list-style-type: none"> – Wind Energy Harvesting for Autonomous Wireless Sensor Networks <i>Adnant Jushi, Alain Pegatoquet and Trong-Nhan Le</i> – A Reliable MAC for Energy-Efficient WSNs in the Era of IoT <i>Philip Parsch and Alejandro Masrur</i> – Using SystemC Cyber Models in an FMI Co-Simulation Environment: Results and Proposed FMI Enhancements <i>Stefano Centomo, Julien Deantoni and Robert De Simone</i>
10:00-11:00	EPDSD-2 (Atrium C) - European Projects in Digital System Design - 3 (Session chair: Francesco Loporati) <ul style="list-style-type: none"> – PROXIMA: Improving Measurement-Based Timing Analysis through Randomisation and Probabilistic Analysis <i>Francisco J Cazorla, Jaume Abella, Jan Anderson, Tullio Vardanega, Francis Vatrinet, Iain Bate, Ian Broster, Mikel Azkarate-Askasua, Franck Wartel, Liliana Cucu, Fabrice Cros, Glenn Farrall, Adriana Gogonel, Andrea Gianarro, Benoît Triquet, Carles Hernandez, Code Lo, Cristian Maxim, David Morales, Eduardo Quiñones, Enrico Mezzetti, Leonidas Kosmidis, Irune Agirre, Mikel Fernandez, Mladen Slijepcevic, Philippa Conmy and Walid Talaboulma</i> – CONTREX: Design of embedded mixed-criticality CONTROL systems under consideration of EXtra-functional properties <i>Ralph Görge, Kim Grüttner, Fernando Herrera, Gianluca Palermo, William Fornaciari, Carlo Brandolese, Davide Gadioli, Sara Bocchio, Luca Ceva, Paolo Azzoni, Massimo Poncino, Sara Vinco, Enrico Macii, Salvatore Cusenza, John Favaro, Raul Valencia, Ingo Sander, Kathrin Rosvall, Davide Quaglia, Pablo Peñil, Julio Medina and Eugenio Villar</i> – SAFEPOWER project: Architecture for Safe and Power-Efficient Mixed-Criticality Systems <i>Alina Lenz, Mikel Azkarate-Askasua Blázquez, Javier Coronel, Alfons Crespo, Simon Davidmann, Juan Carlos Diaz Garcia, Nera González Romero, Kim Grüttner, Roman Obermaisser, Johnny Öberg, Jon Perez, Ingo Sander and Ingemar Söderquist</i>
11:00-11:30	Coffee Break (DSD Poster Session 2) (Lobby Area)

<p>11:30-13:00</p>	<p>SPPI-5 (Panorama) - Software Testing & Test Automation (Session Chair: Helena Holmström Olsson)</p> <ul style="list-style-type: none"> – 11:30-12:00 Harnessing Automated Test Case Generators for GUI Testing in Industry <i>Claus Klammer, Rudolf Ramler and Heinz Stummer</i> – 12:00-12:15 Specifying executable or nonexecutable acceptance test cases for event processing applications <i>Johannes Weis, Alexander Schill</i> – 12:15-12:30 Application of Mahalanobis-Taguchi Method and 0-1 Programming Method to Cost-Effective Regression Testing <i>Hirohisa Aman, Yuta Tanaka, Takashi Nakano, Hideto Ogasawara and Minoru Kawahara</i> – 12:30-12:45 Evidence-based Timelines for Software Process Improvement Retrospectives: A Case Study for Integrating User eXperience Practices into Software Development <i>Pariya Kashfi, Robert Feldt, Agneta Nilsson and Richard Berntsson Svensson</i>
<p>11:30-13:00</p>	<p>ESE-2 (Megaron G) - Software Engineering in Automotive and Industry (Session Chair: Etienne Borde)</p> <ul style="list-style-type: none"> – 11:30-12:00 Modeling of End-to-end Resource Reservations in Component-based Vehicular Embedded Systems <i>Saad Mubeen, Mohammad Ashjaei, Thomas Nolte, John Lundbäck, Mattias Gålnander and Kurt-Lennart Lundbäck</i> – 12:00-12:30 Pruning Architectural Models of Automotive Embedded Systems via Dependency Analysis <i>Raluca Marinescu, Saad Mubeen and Cristina Seceleanu</i> – 12:30-13:00 Exploring Test Overlap in System Integration: An Industrial Case Study <i>Daniel Flemström, Wasif Afzal and Daniel Sundmark</i>
<p>11:30-13:00</p>	<p>EPDS-3 (Atrium A) - European Projects in Digital System Design - 2 (Session chair: Gustavo Marrero Callico)</p> <ul style="list-style-type: none"> – OPERA: a Low Power Approach to the Next Generation Cloud Infrastructures <i>Alberto Scionti, Olivier Terzo and Pietro Ruiu</i> – Synthesis and Performance Optimization of a Switching Nano-crossbar Computer <i>Dan Alexandrescu, Mustafa Altun, Lorena Anghel, Anna Bernasconi, Valentina Ciriani, Luca Frontini and Mehdi Tahoori</i> – Efficiency Modeling and Analysis of 64-bit ARM Clusters for HPC <i>Joël Wanza Weloli, Sébastien Bilavarn, Said Derradji, Cécile Belleudy and Sylvie Lesmanne</i> – Luminous tiles: a new smart device for buildings and architectures <i>Tullio Facchinetti, Guido Benetti, Alessandro Tramonte, Luca Carraro, Mauro Benedetti, Enrico Randone, Marcello Simonetta, Giorgio Capelli and Guido Giuliani</i>
<p>11:30-13:00</p>	<p>DTFT-2 (Atrium B) - Dependability, Testing and Fault Tolerance in Digital Systems -2 (Session chair: Hana Kubatova)</p> <ul style="list-style-type: none"> – Pre-emptive Garbage Collection for SSD RAID <i>Muhammed Ziya Komsul and Alistair Mcewan</i> – Efficient Checkpointing-Based Safety-Verification Flow Using Compiled-Code Simulation <i>Bogdan-Andrei Tabacaru, Moomen Chaari, Wolfgang Ecker, Thomas Kruse and Cristiano Novello</i> – Does Cascading Schmitt-Trigger Stages Improve the Metastable Behavior? <i>Andreas Steininger, Robert Najvirt and Jürgen Maier</i> – Regression Test Suites Optimization for Application-specific Instruction-set Processors and Their Use for Dependability Analysis <i>Marcela Zachariasova, Zdenek Kotasek and Michaela Kekelyova-Belesova</i>
<p>11:30-13:00</p>	<p>MCS DIA (Atrium C) - Mixed Criticality System Design, Implementation and Analysis (Session chair: Eugenio Villar)</p> <ul style="list-style-type: none"> – Introducing Utilization Caps into Mixed-Criticality Scheduling <i>Mitra Mahdiani and Alejandro Masrur</i> – A Realistic Approach to a Network-on-Chip Cross-Domain Pattern <i>Asier Larrucea Ortube, Hamidreza Ahmadian, Roman Obermaisser, Jon Perez and Carlos Fernando Nicolas</i> – pTNoC: Probabilistically Time-Analyzable Tree-Based NoC for Mixed-Criticality Systems <i>Mladen Slijepcevic, Mikel Fernandez, Carles Hernandez, Jaume Abella, Eduardo Quiñones and Francisco J Cazorla</i>
<p>13:00-14:30</p>	<p>Lunch Break</p>

14:30-15:30	<p>Keynote SEAA (Panorama) (Session chair: Martin Höst) <i>Carl-Eric Mols</i> Gaining maturity in Open Source – a model based on Sony Mobile’s journey</p>
15:30-16:30	<p>CPS-2 (Panorama) - Industrial Control and IoT (Session Chair: Karl-Erwin Großpietsch)</p> <ul style="list-style-type: none"> – 15:30-16:00 Kiuas: IoT Cloud Environment for Enabling the Programmable World <i>Petri Selonen and Antero Taivalsaari</i> – 16:00-16:30 Software Containers for Industrial Control <i>Thomas Goldschmidt and Stefan Hauck-Stattelmann</i>
15:30-16:30	<p>ETCS (Atrium A) - ETCS – Emerging Technologies and Circuit Synthesis (Session chair: Odysseas Koufopavlou)</p> <ul style="list-style-type: none"> – Stochastic Event-based Architecture for Bayesian Online Change Point <i>Andreas Andreou and Tomas Figliolia</i> – Quantum Machine Learning Based on Minimizing Kronecker-Reed-Muller Forms and Grover Search Algorithm with Hybrid Oracles <i>Bryan Lee and Marek Perkowski</i> – Logic Synthesis for Switching Lattices by Decomposition with P-Circuits <i>Anna Bernasconi, Valentina Ciriani, Luca Frontini, Valentino Liberali, Gabriella Trucco and Tiziano Villa</i>
15:30-16:30	<p>AHSA-2 (Atrium B) - Architectures and Hardware for Security Applications - 2 (Session chair: Robert Lorencz)</p> <ul style="list-style-type: none"> – Remote Dynamic Clock Reconfiguration based Attacks on Internet of Things Applications <i>Anju Johnson, Sikhar Patranabis, Rajat Subhra Chakraborty and Debdeep Mukhopadhyay</i> – Secured Miniaturized System-in-Package Contactless and Passive Authentication Devices featuring NFC <i>Juergen Schilling, Walther Pachler, Bernhard Roitner, Thomas Rupprechter, Holger Bock, Gerald Holweg and Norbert Druml</i> – Cylindrical Reconvergence Physical Unclonable Function <i>Rodrigo Surita, Mario Côrtes, Diego Aranha and Guido Araujo</i>
15:30-16:30	<p>EMBEREA-2 (Atrium C) - Embedded and Real Time systems - 2 (Session chair: Alistair McEwan)</p> <ul style="list-style-type: none"> – Resolving Performance Interference in SR-IOV Setups with PCIe Quality-of-Service Extensions <i>Andre Richter, Christian Herber, Thomas Wild and Andreas Herkersdorf</i> – Using Model-Based Testing for Manufacturing and Integration-Testing of Embedded Control Systems <i>Tobias Rauter, Andrea Höller, Johannes Iber and Christian Kreiner</i> – Improved Task Scheduler for Dual-Core Real-Time Systems <i>Lukas Kohutka and Viera Stopjaková</i>
16:30-17:00	Coffee Break (incl. Tool Demos) (Lobby Area)
18:00-23:00	Excursion & Conference Dinner

Registration/ Support Desk hours (08:30 - 13:00 & 14:00 - 16:00)	
09:00-10:00	Keynote DSD (Panorama) (Session chair: Francesco Leparati) <i>Ing.Eur.Dr. Gustavo Marrero Callico</i> Hyperspectral Imaging for Brain Cancer Detection
10:00-11:00	SMSE-3 (Panorama) - Product Lines (Session Chair: Steve Counsell) <ul style="list-style-type: none"> – 10:00-10:30 Functional Safety in Product Lines - A Systematic Mapping Study <i>Stephan Baumgart and Joakim Fršberg</i> – 10:30-11:00 On the Dependability for Dynamic Software Product Lines - A Comparative Systematic Mapping Study, Jane Eleuterio, Felipe Gaia <i>Andrea Bondavalli, Paolo Lollini, Genaina Rodrigues and Cecilia Rubira</i>
10:00-11:00	DTFT-3 (Atrium A) - Dependability, Testing and Fault Tolerance in Digital Systems -2 (Session chair: Marcela Zachariasova) <ul style="list-style-type: none"> – A Majority-Based Reliability-Aware Task-Mapping in High-Performance Homogenous NoC Architectures <i>Alireza Namazi, Meisam Abdollahi, Saeed Safari and Siamak Mohammadi</i> – Verification of Robot Controller for Evaluating Impacts of Faults in Electro-mechanical Systems <i>Jakub Podivinsky, Ondrej Cekan, Jakub Lojda and Zdenek Kotasek</i> – Error Correction Method Based On The Short-Duration Offline Test <i>Jan Bělohoubek, Petr Fišer and Jan Schmidt</i>
10:00-11:00	AHSA-3 (Atrium B) - Architectures and Hardware for Security Applications - 3 (Session chair: Odysseas Koufopavlou) <ul style="list-style-type: none"> – Testability based Metric for Hardware Trojan Vulnerability Assessment <i>Sayandeep Saha, Rajat Subhra Chakraborty and Debdeep Mukhopadhyay</i> – HLS-based Methodology for Fast Iterative Development Applied to Elliptic Curve Arithmetic <i>Simon Pontié, Alban Bourge, Adrien Prost-Boucle, Paolo Maistri, Olivier Muller, Régis Leveugle and Frédéric Rousseau</i> – True Random Number Generator based on ROPUF circuit <i>Simona Buchovecka, Robert Lorencz, Filip Kodytek and Jiri Bucek</i>
10:00-11:00	EPDSD-4 (Atrium C) - European Projects in Digital System Design - 3 (Session chair: Lech Jozwiak) <ul style="list-style-type: none"> – The EMC2 Project on Embedded Microcontrollers: Technical Progress after two years <i>Werner Weber, Alfred Hoess, Frank Oppenheimer, Norbert Druml, Erwin Schoitsch, Sascha Uhrig and Mark van Helvoort</i> – The SafeCOP ECSEL Project--Safe Cooperating Cyber-Physical Systems using Wireless Communication <i>Paul Pop, Detlef Scholle, Hans Hansson, Gunnar Widfors and Malin Rosqvist</i> – AXIOM: A Hardware-Software Platform for Cyber Physical Systems <i>Somnath Mazumdar, Eduard Ayguade, Nicola Bettin, Javier Bueno, Sara Ermini, Antonio Filgueras, Daniel Jiminez-Gonzalez, Carlos Alvarez Martinez, Xavier Martorell, Francesco Montefoschi, David Oro, Dionisios Pnevmatikatos, Antonio Rizzo, Dimitris Theodoropoulos and Roberto Giorgi</i>
11:00-11:30	Coffee Break (DSD Poster Session 3) (Lobby Area)
11:30-12:45	A-BPM (Panorama) - Advancing Business Process Management (Session Chair: Werner Schmidt) <ul style="list-style-type: none"> – 11:30-12:00 Automating BPMN Interchange Testing <i>Matthias Kurz</i> – 12:00-12:30 AAISP: An Approach for Aligning Information Systems Perspectives <i>Stefanie Betz, Susan Hickl, Andreas Oberweis</i> – 12:30-12:45 It is all about the Interface: Factors which Drive Process Interface Criticality in Organizations <i>Matthias Lederer, Laura Heider</i>
11:30-12:45	ESPRESSE (Megaron G) - Estimation and Prediction in Software and Systems Engineering (EsPreSSE) (Session Chair: Dietmar Winkler) <ul style="list-style-type: none"> – 11:30-12:00 Managing the Uncertainty of Bias-Variance Tradeoff in Software Predictive Analytics <i>Nikolaos Mittas and Lefteris Angelis</i> – 12:00-12:15 Requirements for Integrating Defect Prediction and Risk-based Testing <i>Rudolf Ramler and Michael Felderer</i> – 12:15-12:30 Automated Bug Triaging in an Industrial Context <i>Bruno Rossi and Václav Dedík</i> – 12:30-12:45 A Replication Study: Where and When Should Defects be Re-Assigned <i>Tamer Abdou and Ayse Bener</i>

11:30-13:00	<p>ASHWPA (Atrium A) - Advanced Systems in Healthcare, Wellness and Personal Assistance (Session chair: Francesco Loporati)</p> <ul style="list-style-type: none"> – Low-Power Online ECG Analysis Using Neural Networks <i>Mehdi Modarressi, Ali Yasoubi, and Maryam Modarressi</i> – Ultra-Low Power Estimation of Heart Rate Under Physical Activity Using a Wearable PhotoPlethysmoGraphic System <i>Elisabetta De Giovanni, Srinivasan Murali, Francisco Rincon and David Atienza</i> – Energy Modeling and Architecture Exploration for Emotion Detection Systems <i>Chaka Koné, Cécile Belleudy and Nhan Le-Thanh</i> – A Control of Electric Wheelchair Using an EMG Based on Degree of Muscular Activity <i>Chiharu Ishii and Ryoichi Konishi</i>
11:30-13:00	<p>RECONFIG-2 (Atrium B) - Reconfigurable Computing-2 (Session chair: Ondrej Novak)</p> <ul style="list-style-type: none"> – Elastic Management and QoS Provisioning Scheme for Adaptable Multi-core Protocol Processing Architecture <i>Mohammad Badawi, Zhonghai Lu and Ahmed Hemani</i> – High-Performance FPGA Architecture for Multi-Line Beamforming in Ultrasound Applications <i>Valentino Meacci, Luca Bassi, Stefano Ricci, Enrico Boni and Piero Tortoli</i> – Dynamically Reconfigurable Architecture with Atomic Configuration Updates for Flexible Regular Expressions Matching in FPGA <i>Vlastimil Košar and Jan Korenek</i> – Multi-Granular Arithmetic in a Coarse-Grain Reconfigurable Architecture <i>Stef Louwers, Luc Waeijen, Mark Wijtvlit, Ruud Koolen and Henk Corporaal</i>
11:30-13:00	<p>ASAIT (Atrium C) - Architectures and Systems for Automotive and Intelligent Transportation (Session chair: Smail Niar)</p> <ul style="list-style-type: none"> – Adaptive Control of the Heating System for Optimized Energy Consumption in Electric Vehicles <i>Rhea Valentina, Alexander Viehl, Oliver Bringmann and Wolfgang Rosenstiel</i> – Braking in Close Following Platoons: The Law of the Weakest <i>Dharshan Krishna Murthy and Alejandro Masrur</i> – V2I Cooperation for Traffic Management with SafeCOP <i>Giovanni Agosta, Alessandro Barengi, Carlo Brandolese, William Fornaciari, Gerardo Pelosi, Stefano Delucchi, Massimo Massa, Maurizio Mongelli, Enrico Ferrari, Leonardo Napoletani, Luciano Bozzi, Carlo Tieri, Dajana Cassioli and Luigi Pomante</i> – SecUp: Secure and Efficient Wireless Software Updates for Vehicles <i>Marco Steger, Carlo Boano, Michael Karner, Joachim Hillebrand, Werner Rom, and Kay Römer</i>
13:00-14:30	Lunch Break
14:30-16:10	<p>SEAA Work in Progress session (Panorama) (Session Chair: Konrad Kloeckner)</p> <ul style="list-style-type: none"> – <i>Lucas Gren</i>, Using the Agile Adoption Framework to Assess Agility and Guide Improvements – <i>Sousuke Amasaki, Yuto Miyake</i>, A Preliminary Survey on Evaluation Procedures in Cross-Company Defect Prediction Studies – <i>Krasimir Baylov, Aleksandar Dimov</i>, Reference Architecture for Self-Adaptive Microservice Systems – <i>Janusz Sosnowski, Pawel Janczarek, Bartosz Dobrzynski</i>, Exploring Problem Repositories in Software Projects – <i>Farshad Saeidi, Farzad Sanati</i>, Embedded System in Software Products – <i>Stanislav Chren, Barbora Buhnova, Bruno Rossi</i>, Identification of Reliability Bottlenecks in Smart Cities – <i>Jan Bosch, Helena Holmström Olsson</i>, Towards Automated A/B/n Testing in Families of Smart Systems – <i>Christian Berger, Ola Benderus</i>, Encapsulated Software Build Environments on the example of a Self-Driving Heavy Vehicle – <i>Alessio Bucaioni, Antonio Cichetti, Federico Ciccozzi, Saad Mubeen, Alfonso Pierantonio, Mikael Sjödin</i>, Towards Design-Space Exploration of Component Chains in Vehicle Software – <i>Kai Höfig, Marc Zeller</i>, SpARTA – State-Aware Fault Tree Analysis
14:30-16:00	<p>DSD Work in Progress session (Megaron G) (Session Chair: Erwin Grosspietsch)</p> <ul style="list-style-type: none"> – <i>Soeren Aagaard Mikkelsen, Rune Hylsberg Jacobsen</i>, NLM-Based Authorization Framework for Improved User Content – <i>Ievgen Kabin, Estuardo Alpirez Bock, Christian Wittke, Dan Kreiser, Zoya Dyka, Peter Langendoerfer</i>, On the Influence of Hardware Technologies on the Vulnerability of Protected ECC Implementations – <i>Calypto Barnes, Jean-Marie Cottin, Francois Verdier, Alain Pegatoquet, Daniel Gaffé</i>, Developing a

Framework Dedicated to Wireless Protocol Property Validation during Simulation

- *Carlos Andrés Lara-Nino, Arturo Díaz-Pérez, Miguel Morales-Sandoval*, Energy Efficient Security Module Configurations for Sensor Nodes
- *Martin Krcma, Zdenek Kotasek, Jakub Lojda, Jan Kastil*, Comparison of FPNNs Approximation Capabilities
- *Alexandru Amaricai*, Towards a Template Based CPU-FPGA Acceleration Framework for Irregular Parallel Applications
- *Mohammad Azimi, Mehdi Modarressi*, Proactive Network-on-Chip Path Setup for Message Passing Programs
- *Christian Schöler, René Krenz-Baath, Roman Obermaisser*, A Dominator-Based Partitioning for Efficient Scheduling in Time-Triggered NoCs

Poster List

Poster sessions will be held in the **Lobby Area**. Poster numbers can be found below and on the respective poster boards.

Please put up your posters in the morning of the day on which your poster session is scheduled. Please be present near your poster during your respective poster session.

You must take your poster down at the end of the day. Posters not taken down at the end of the day will be recycled.

DSD Poster Session 1 (Wednesday, August 31 st - 11:00-11:30)	
1	Probing Approximate TMR in Error Resilient Applications for Better Design Tradeoffs <i>Tooba Arifeen, Abdus Sami Hassan, Hossein Moradian, and Jeong A. Lee</i>
2	Using timing-driven inter-FPGA routing for multi-FPGA prototyping exploration <i>Umer Farooq, Roselyne Chotin-Avot and Habib Mehrez</i>
3	Novel FPGA-based low-cost hardware architecture for the PRESENT block cipher <i>Carlos Andres Lara Nino, Miguel Morales-Sandoval and Arturo Diaz Perez</i>
4	Unum: Adaptive Floating-Point Arithmetic <i>Enric Morancho</i>
5	Synthesis of Multiple-valued Logical Networks for FPGA Implementations <i>Stanisław Deniziak, Mariusz Wiśniewski and Karol Wieczorek</i>
6	Test Decompressor Effectivity Improvement <i>Ondřej Novák, Jiří Jeníček and Martin Rozkovec</i>
7	On Power-Analysis Resistant Hardware Implementations of ECC-Based Cryptosystems <i>Roman Willi, Andreas Curiger and Paul Zbinden</i>

DSD Poster Session 2 (Thursday, September 1 st - 11:00-11:30)	
1	Polynomial based NUC implemented on FPGA <i>Martin Rozkovec and Jiří Čech</i>
2	A Library to Model and Configure Large Regular Structures in SystemC <i>Christian Amstutz and Oliver Sander</i>
3	TERO-based Detection of Hardware Trojans on FPGA Implementation of the AES Algorithm <i>Paris Kitsos, Kyriakos Stefanidis and Artemios Voyiatzis</i>
4	Device Context Classification for Mobile Power Consumption Reduction <i>Ismat Yahia Chaib Draa, Maroua Nouri, Smail Niar and Abdelghani Bekrar</i>
5	Low-Cost Platform-Independent FPGA Based Real-Time Systems Tester <i>Diaa Jadaan, Kyrre Gonsholt and Amund Skavhaug</i>
6	Enhanced duplication method with TMR-like masking abilities <i>Pavel Vít, Jaroslav Borecký, Hana Kubatova and Martin Kohlík</i>
7	Design and Physical Implementation of a Target ASIC for SET Experiments <i>Varadan Savulimedu Veeravalli and Andreas Steininger</i>
8	Temperature dependence of ROPUF on FPGA <i>Filip Kodýtek, Róbert Lórencz, Jiří Buček and Simona Buchovecká</i>

DSD Poster Session 3 (Friday, September 2nd - 11:00-11:30)

1	Testing for Intermittent Resistive Faults in CMOS Integrated Systems <i>Hans G. Kerkhoff and Hassan Ebrahimi</i>
2	Verifying Linear Temporal Logic Properties in UML/OCL Class Diagrams using Filmstripping <i>Frank Hilken and Martin Gogolla</i>
3	An Interactive Design Space Exploration Tool for Dependable Integrated Circuits <i>Stefan Scharoba and Heinrich T. Vierhaus</i>
4	Model-Based Evaluation Of System Scalability: Bandwidth Analysis For Smartphone-Based Bioimpedance Applications <i>François Patou, Maria Dimaki, Winnie E. Svendsen and Jan Madsen</i>
5	Implementation of a Boolean Masking Scheme for the SCREAM Cipher <i>William Diehl and Kris Gaj</i>
6	An Energy Efficient Neuromorphic Processor for Cognitive Cyber-Physical Systems <i>Martin Villemur, Tomas Figliola, Pedro Julian and Andreas Andreou</i>

Social Events

Welcome Cocktail

Wednesday, August 31st

Time: 19:00

Location: St. Raphael Resort – Venue

Welcome Reception is the first social gathering between all conference delegates and it will take place at the Venue Hotel. It will be a relaxing evening during which delegates will have the opportunity to talk to colleagues and peers, while enjoying local drinks and ample canapés.

Ticket: € 30

Welcome Cocktail is included in EUROMICRO Member and Non-Member Registration Fees; is not included in Students' Registration.

Tickets can be purchased through the Easy Conferences online system www.easyconferences.org. Also, you may buy ticket(s) onsite at the registration/support desk.

Excursion & Conference Dinner

Thursday, September 1st

Departure Time: 18:00 (meet at the Lobby of St. Raphael Resort – Venue)

Location: Local Restaurant

We will get together at 17:45 at the lobby of St Raphael Resort – Venue, from where we will promptly depart in air-conditioned coaches for a fascinating tour. Experienced professional guides will tell us about the history of Cyprus in general and Limassol town in particular. We will visit Limassol Marina and Limassol Castle.

Our tour will finish with a dinner which will consist of an array of traditional dishes complimented with local drinks and desserts. During the dinner we will enjoy an entertaining programme with live music and singing performed by a local band and traditional dances performed by a local dance group.

Ticket: € 50

Conference Dinner is included in EUROMICRO Member and Non-Member Registration Fees; is not included in Students' Registration.

Tickets can be purchased through the Easy Conferences online system www.easyconferences.org. Also, you may buy ticket(s) onsite at the registration/support desk.

Half Day Limassol Excursion (Optional)

Friday, September 2nd

Departure Time: All participants should gather in the Lobby of the St. Raphael Resort by **14:20** and be ready **for departure at 14:30**.



Limassol is the second largest city of the island, with stretches of sandy beaches, beautiful traditional and modern buildings as well as world-class hotels. Limassol is known for its lively Carnival celebrations, the annual Wine Festival and its location, which is between two ancient city-kingdoms: 'Amathus' to the east and 'Kourion' to the west, making it a popular tourist attraction.

We will start our journey through the acres of lemon tree plantations in the area of Fasouri, where we will visit the Castle of Kolossi, a fine example of military architecture. Continuing our visit near the old port, we will encounter the **Medieval Museum** and we will walk through the graphic pathways of the old city which has recently undergone restoration.

We will continue our tour with a visit to the **Sanctuary of God Apollon Ylatis**, the god of woodland and protector of the Kourion kingdom, followed by a visit to the **Kourion Greco-Roman theatre** which has been completely restored and is used for musical and theatrical performances in a unique location.

Cost per Person: € 25

Participation fee is inclusive of: Transfers in luxurious fully air-conditioned coaches, officially C.T.O licensed tour guide, entrance fees to all sites, V.A.T. currently 19% and taxes and organization and assistance by Easy Conferences.

Tickets can be purchased through the Easy Conferences online system www.easyconferences.org. Also, you may buy ticket(s) onsite at the registration/support desk.