



DSD 2016

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19th Euromicro Conference on Digital System Design

Limassol, Cyprus, Aug. 31st - Sept. 2nd, 2016

Call for Papers



SCOPE

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system engineering, covering the whole design trajectory from specification down to micro-architectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced investigations, developments and applications.

It focuses on today's and future challenges of advanced system architectures for embedded and high-performance HW/SW systems, application analysis and parallelization, design automation for all design levels, as well as, on modern implementation technologies from full custom in nanometer technology nodes, through FPGAs, to multi-core infrastructures. It covers a multitude of highly relevant design aspects from system, hardware and embedded-software specification, modeling, analysis, synthesis and validation, through system adaptability, security, dependability and fault tolerance, to system energy consumption minimization and multi-objective optimization.

Authors are kindly invited to submit their work according (but not limited) to the seven main topics of the conference main track. In addition, eight Special Sessions (with their own coordinators and subprogram committees) do also welcome contributions in specific themes of particular interest. All papers are reviewed following guidelines, quality requirements and thresholds that are common to all committees.

MAIN TOPICS

- T1: Advanced applications of embedded and cyber-physical systems
- T2: Application analysis and parallelization for embedded and high-performance design
- T3: Specification, modeling, verification and test for systems, hardware and embedded software
- T4: Design and synthesis of systems, hardware and embedded software
- T5: Systems-on-a-chip and networks-on-a-chip
- T6: Programmable/re-configurable/adaptable architectures
- T7: New issues introduced by emerging technologies

SPECIAL SESSIONS/ORGANIZERS

- DTFT: Dependability, Testing and Fault Tolerance in Digital Systems – *H. Kubatova (CTU Prague, CZ), Z. Kotasek (TU Brno, CZ)*
- MCS DIA: Mixed Criticality System Design, Implementation and Analysis – *K. Grüttner (OFFIS, DE), E. Villar (TEISA U Cantabria, ES)*
- AHSA: Architectures and Hardware for Security Applications – *P. Kitsos (TEI of Western Greece, GR), M. Novotny, (Czech Technical University in Prague, CZ)*
- DCPS: Design of Cyber-Physical Systems – *M. Geilen, (TUE, NL), D. Quaglia (U Verona, IT)*
- ASHWPA: Advanced Systems in Healthcare, Wellness and Personal Assistance – *F. Loporati (U Pavia, IT)*
- ASAIT: Architectures and Systems for Automotive and Intelligent Transportation – *S. Niar (U Valenciennes, FR)*
- SDSG: System Design for the Smart Grid – *R. Jacobsen (Aarhus U, DK), E. Ebeid (Aarhus U, DK)*
- EPDSD: European Projects in Digital System Design – *F. Loporati (U Pavia, IT), L. Jozwiak (TUE, NL)*

SUBMISSION GUIDELINES

Authors are encouraged to submit their manuscripts to <https://easychair.org/conferences/?conf=dsd2016>. Should an unexpected web access problem be encountered, please contact the Program Chair by email (dsd2016@easychair.org).

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included.

Conference Publishing Services (CPS) will publish accepted papers in the conference proceedings and the proceedings will be submitted to the IEEE Xplore Digital library and indexing services. Extended versions of selected best papers will be published in a special issue of the ISI indexed "Microprocessors and Microsystems: Embedded Hardware Design" Elsevier journal.

IMPORTANT DATES

Deadline for paper submission: April 24th (extended)

Notification of acceptance: June 13th

Camera ready papers: July 1st

MORE INFORMATION (WEB PAGES)

- DSD 2016: <http://dsd2016.cs.ucy.ac.cy>
- Euromicro: www.euromicro.org